



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/725,685	12/01/2003	2811	385	D486.	3	15	2

CONFIRMATION NO. 3413

Carole A. Mulchinski, M1/040  
The Aerospace Corporation  
2350 East El Segundo Boulevard  
El Segundo, CA 90245

## FILING RECEIPT



\*OC000000012011883\*

Date Mailed: 03/03/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Donald C. Mayer, Palos Verdes Estates, CA;  
Jon V. Osborn, Thousand Oaks, CA;  
Ronald C. Lacoe, Newbury Park, CA;  
Everett E. King, Granada Hills, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 03/02/2004

Projected Publication Date: 06/02/2005

Non-Publication Request: No

Early Publication Request: No

\*\* SMALL ENTITY \*\*

Title

segmented  
Annular segment MOSFET

Title should be Annular Segmented MOSFET